



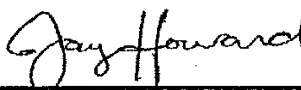
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Joseph W. Tiece, et al.
Serial No.: 09/955,328
Filing Date: September 18, 2001
Group Art Unit: 2115
Examiner: Chun Cao
Title: *Adaptable Bootloader*

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Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this Information Disclosure Statement is being deposited with the United States Postal Service as Express Mail EV352389292US addressed to: Commissioner of Patents, Alexandria, VA 22313-1450, on the date shown below.



Jay Howard

4/30/04

Date

Dear Sir:

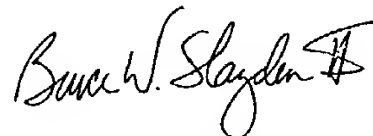
INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Copies of the references are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97 (g) and (h), no representation is made that these references are material to the patentability of the present application.

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Applicants believe no fee is due at this time. However, the Commissioner is hereby authorized to charge any additional fees or credit any overpayments to Deposit Account No. 02-0383 of Baker Botts L.L.P.

Respectfully submitted,
BAKER BOTTS L.L.P.
Attorneys for Applicants



Bruce W. Slayden II
Reg. No. 33,790

Date: April 30, 2004

Correspondence Address:

Customer Number **23640**

512.322.2606

512.322.8306 (Fax)

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PTO-1449 Information Disclosure Citation in an Application	Application No.	Applicant(s)	
	09/955,328	Joseph W. Tiece, et al.	
	Docket Number	Group Art Unit	Filing Date
	068354.0169	2115	September 18, 2001

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.						
B.						
C.						
D.						
E.						
F.						
G.						
H.						

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FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
I.	88/09008	11/17/88	WO	G06F	9/44	X	
J.	0999500	5/10/00	EP	G06F	12/08	X	
K.							
L.							
M.							
N.							
O.							
P.							

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author Title Source and Pertinent Pages)	DATE
Q.	Microchip Technology, Inc., "High-Performance ROM-less Microcontrollers with External Memory Bus", XP-002264754, pages 1-83	2001
R.	Microchip Technology, Inc., "Silicon/Data Sheet Errata" PIC18C601/801, XP002264897, pages 1-10	2002
S.	Microchip Technology, Inc., "Implementing the External Memory Interface on PIC18C601/801 MCUs" XP002264755, pages 1-32	2001
T.	Microchip Technology, Inc., Web page about application note AN778 at < http://www.microchip.com/1010/suppdoc/appnote/all/an >, 1 page, XP002264898	11/19/03
U.	PCT search report for PCT/US02/28501, 9 pages	Mailed 4/2/04

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.